

N- and P-Channel Enhancement Mode MOSFET

General Description

The CMS4614 uses advanced trench technology to provide excellent RDS(ON) and low gate charge.

The complementary MOSFETs may be used in inverter and other applications.

Features

- Dual N and P Channel MOSFET
- Surface mount Package
- Reliable and Rugged
- Simple Drive Requirement
- Low On-resistance

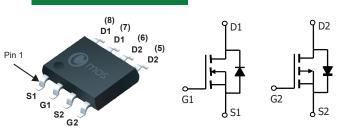
Product Summary

	BVDSS	RDSON	ID
N-Channel	40V	17mΩ	8A
P-Channel	-40V	38mΩ	-7A

Applications

- Power Management
- DC/DC Converter
- Power Management in FAN, LCD Inverter Systems

SOP-8 Pin Configuration



SOP-8	n-channel	p-channe
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Туре	Package	Marking
CMS4614	SOP-8	CMS4614

Absolute Maximum Ratings

Symbol	Parameter	Max n-channel	Max p-channel	Units
V_{DS}	Drain-Source Voltage	40	-40	V
V _{GS}	Gate-Source Voltage	±	V	
I _D @T _A =25℃	Continuous Drain Current	8	-7	Α
I _D @T _C =70°C	Continuous Drain Current	6	-5.5	Α
I _{DM}	Pulsed Drain Current	24	-21	
P _D @T _C =25℃	Total Power Dissipation		2	W
T _{STG}	Storage Temperature Range	-55 t	°C	
T _J	Operating Junction Temperature Range	-55 t	$^{\circ}$	

Thermal Characteristics: n-channel

Symbol	Parameter	Тур.	Max.	Unit
$R_{\theta JA}$	Maximum Junction-to-Ambient (Steady-State)		62.5	°C/W



N- and P-Channel Enhancement Mode MOSFET

Thermal Characteristics: p-channel

Symbol	Parameter		Max.	Unit
$R_{\theta JA}$	Maximum Junction-to-Ambient (Steady-State)		62.5	°C/W

N Channel Electrical Characteristics (TJ=25°C unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V_{GS} =0 V , I_D =250 μ A	40			V
P	Static Drain-Source On-Resistance	V_{GS} =10V , I_D =6A			17	0
R _{DS(ON)}	Static Dialii-Source Off-Resistance	V_{GS} =4.5V , I_D =4A			20	mΩ
V _G S(th)	Gate Threshold Voltage	V_{GS} = V_{DS} , I_D =250 μ A	1		3	V
I _{DSS}	Drain-Source Leakage Current	V _{DS} =36V , V _{GS} =0V			1	uA
I _{GSS}	Gate-Source Leakage Current	V _{GS} =±20V , V _{DS} =0V			±100	nA
gfs	Forward Transconductance	V_{DS} =5V , I_D =3A		13		S
Qg	Total Gate Charge			13		
Q_gs	Gate-Source Charge	V_{DS} =20V , V_{GS} =4.5V , I_{D} =8A		4		nC
Q_gd	Gate-Drain Charge			3		
$T_{d(on)}$	Turn-On Delay Time			5		
Tr	Rise Time	V_{DD} =20V , V_{GS} =10V , R_L =2.5 Ω		4		no
$T_{d(off)}$	Turn-Off Delay Time	R _{GEN} =3Ω		16		ns
T _f	Fall Time			3		
C _{iss}	Input Capacitance			1150		
C _{oss}	Output Capacitance	V_{DS} =20V , V_{GS} =0V , f=1MHz		113		pF
C _{rss}	Reverse Transfer Capacitance			12		

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Is	Continuous Source Current	V _G =V _D =0V , Force Current			8	Α
I _{SM}	Pulsed Source Current				24	Α
V _{SD}	Diode Forward Voltage	V _{GS} =0V , I _S =5A			1.2	V

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N- and P-Channel Enhancement Mode MOSFET

P Channel Electrical Characteristics (TJ=25°C unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V_{GS} =0V , I_D =-250 μ A	-40			V
D	Static Drain-Source On-Resistance	V _{GS} =-10V , I _D =-5A			38	
R _{DS(ON)}	Static Drain-Source On-Resistance	V _{GS} =-4.5V , I _D =-2A			53	mΩ
VGS(th)	Gate Threshold Voltage	$V_{GS}=V_{DS}$, $I_D=-250\mu A$	-1		-3	V
I _{DSS}	Drain-Source Leakage Current	V _{DS} =-32V , V _{GS} =0V			-1	uA
I _{GSS}	Gate-Source Leakage Current	V_{GS} =±20V , V_{DS} =0V			±100	nA
gfs	Forward Transconductance	V_{DS} =-5V , I_{D} =-3A		10		S
Q_g	Total Gate Charge (10V)			14		
Q_{gs}	Gate-Source Charge	V _{DS} =-20V, V _{GS} =-10V, I _D =-8A		4		nC
Q_gd	Gate-Drain Charge			3		
$T_{d(on)}$	Turn-On Delay Time			8		
T _r	Rise Time	V_{DD} =-20V , V_{GS} =-10V, R_{L} =2.3 Ω		6		ne
$T_{d(off)}$	Turn-Off Delay Time	R _{GEN} =6Ω		18		ns
T _f	Fall Time			8		
C _{iss}	Input Capacitance			2400		
Coss	Output Capacitance	V _{DS} =-20V , V _{GS} =0V , f=1MHz		100		pF
C _{rss}	Reverse Transfer Capacitance			65		

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Is	Continuous Source Current	V _G =V _D =0V , Force Current			-7	Α
I _{SM}	Pulsed Source Current				-21	Α
V_{SD}	Diode Forward Voltage	V _{GS} =0V , I _S =-1A			-1.2	V

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